# 59 DAC REGISTRATION
## *QUESTIONNAIRE RESULTS*

| | |
|---|---|
| Conference Attendees | 1838 |
| Exhibit Only/I LOVE DAC Attendees | 1960 |
| Total Attendance | 3798 |
| Booth Staff (exhibitors) | 1072 |
| **Total Participants** | **4870** |

## In which **primary industry segment** does your group or organization work?

| | |
|---|---|
| Academic | 16% |
| Automotive | 2% |
| Consumer Electronics | 3% |
| Cyber Security | 1% |
| Design in the Cloud | 2% |
| Design IT | 1% |
| Design Services: Includes ASIC Services & Post Silicon Services | 4% |
| EDA Software | 13% |
| Embedded Software or System Design | 2% |
| Foundry/IC Manufacturing | 3% |
| Internet of Things | 1% |
| IP/Core Design | 5% |
| Machine Learning and AI | 3% |
| Military/Aero | 1% |
| Networking | 2% |
| Processors (CPU, MCU, AI, etc.) | 6% |
| Other | 4% |

## Which **products and/or services** in the following areas do you recommend, purchase, or influence the purchase of?

| Design Tools/Services | |
|---|---|
| Analog/AMS Design/Simulation | 13% |
| Application Specific Std. Part (ASSP) | 3% |
| Design for Manufacturing & Yield | 8% |
| Digital Logic Simulation | 9% |
| Embedded – Software Design | 9% |
| Engineering IT Hardware | 8% |
| Engineering IT Services | 6% |
| Foundry/Silicon Vendor | 7% |
| Full-Custom Layout | 8% |
| HW/SW co-Design | 16% |
| IP | 15% |
| Machine Learning/AI Software and Tools | 18% |
| Packaging | 5% |
| PCB | 5% |
| Physical Verification | 11% |
| Placement & Routing | 14% |
| Processors (CPU, MCU, AI, etc.) | 11% |
| RISC-V/Open Source IP | 10% |
| RTL & Formal Verification | 15% |
| System Design & Specification | 12% |
| Test | 7% |
| Other | 4% |
| None | 11% |

| Services | |
|---|---|
| Design on Cloud/Design Infrastructure | 14% |
| Embedded Software and System Design | 11% |
| Machine Learning, AI & Applications | 20% |
| Physical Design | 19% |
| Post Silicon Services | 6% |
| System/Logic Design | 15% |
| Verification | 15% |
| Other | 3% |
| None | 16% |

| Other | |
|---|---|
| IP/Cores | 24% |
| Libraries/Models | 15% |
| None | 39% |

## What is your **principal job function**?

| | | | | | |
|---|---|---|---|---|---|
| Application Software Engineer | 3% | Engineering Management (includes CAD, design, & software) | 7% | Professor/Researcher | 9% |
| CAD Engineer, including Application Engineer | 6% | | | Software Engineering IT | 1% |
| Design Engineer (includes chip, module, & board) | 7% | Executive/Senior Management | 8% | Student | 10% |
| Editor/Publisher | 1% | Financial or Industry Analyst | 1% | System Architect | 2% |
| Embedded Hardware Engineer | 1% | Hardware Engineering IT | 1% | Venture Capitalist | 0% |
| Embedded Software Engineer | 1% | Manufacturing, Test, & Assembly | 1% | Verification Engineer | 2% |
| Emulation/FPGA Prototyping Engineer | 0% | Marketing/Sales | 7% | Other | 3% |