44TH DESIGN AUTOMATION CONFERENCE

Only DAC offers:

• A robust technical program covering the latest research, developments and trends in electronic design, ranging from management practices to products, methodologies and technologies.
• Worldwide attendance from developers, designers, researchers, academics, managers and engineers from leading electronics companies and universities.
• A vibrant exhibition with 240 companies displaying products, technologies and services for the electronic design industry.

San Diego Convention Center • San Diego, California • June 4-8, 2007

ADVANCE PROGRAM

www.dac.com

Sponsored by:

www.kdack.com

In Technical Cooperation with:

VTS
DAC: Where The Electronic Design Community Meets...

Dear Colleague:

Soon, thousands of us will converge in San Diego to attend the Design Automation Conference, the largest and most prestigious annual event focused on the design of electronic circuits and systems.

Who are we? We are the executives, managers, developers, designers, academics, journalists, venture capitalists, and others who make our industries and research groups the innovative, exciting, and productive drivers for the creation of electronic systems.

We will learn about the latest in design tools and methodologies for verification and test, design for manufacturing, IP, design libraries, RF/wireless, analog and mixed-signal designs, embedded software in SoCs, and other effects in today’s designs. DAC is the place for the electronic design community to meet with:

• friends and renew acquaintances;
• colleagues and learn about the latest advances in tools, methodologies, fabrication, and test;
• vendors and learn about the latest product offerings;
• other designers and find out how they are using these tools to develop the next generation of ...

This year, we have 240 exhibitors, 201 technical presentations (including 8 on “Wild And Crazy Ideas”), plus 8 technical program panels, 18 pavilion panels, 7 full day tutorials, 6 hands-on tutorials, 7 workshops, and various co-located events.

Of particular interest to many of us will be the Automotive Theme, which highlights design challenges that stem from embedding electronic systems in engine control, driver interfaces, communications, entertainment, navigation, and safety critical systems in our automobiles. DAC will also feature a special Executive Management Seminar with management guru and best-selling author Geoffrey Moore, on the topic of managing innovation in our industry.

We finish the week with a tribute to the legacy of A. Richard Newton, Professor and Dean of the College of Engineering at the University of California, Berkeley, with a Thursday keynote by Jan Rabaey, the Donald O. Pederson Distinguished Professor at the University of California at Berkeley.

For more detailed conference information, visit us online at www.dac.com.

Please join us for an informative and inspiring week.

Best regards,
Steve Levitan
General Chair, 44th DAC

Networking Opportunities and Social Activities

Be sure to attend these DAC functions!

☆ EDA Consortium Executive Reception, Sunday, 5:00pm - 7:00pm
☆ Exhibit Floor Happy Hour on Monday, 5:00pm - 6:00pm
☆ IEEE CEDA Distinguished Speaker Lecture, Monday, 6:00pm - 8:00pm
☆ SIGDA Ph.D. Forum and Member Meeting, Tuesday, 6:30pm - 8:00pm
☆ Management Seminar on Tuesday, 10:30am - 6:45pm
☆ DAC Wednesday Night Party, 7:00pm - 10:00pm. Enjoy great food, drinks and entertainment.
☆ Mass Book Signing, Thursday, 11:00am - 12:30pm
☆ The DAC Pavilion in Booth 6360 on the exhibit floor – there’s always something going on!
☆ Keynote Addresses on Monday, Tuesday and Thursday
This year’s technical program consists of 161 selected papers out of 713 submissions, supplemented by 8 special sessions, 7 tutorials, 8 panels and 18 pavilion panels. The result is an exciting program, targeted to design engineers, management, developers and researchers, that showcases the latest advances in the field of electronic design automation.

The technical theme for this year’s DAC is automotive electronics; an all-day track on Wednesday includes a special session, invited talks, a panel, and regular papers. Modern automobiles have an incredible array of electronic systems: engine management, satellite navigation, adaptive cruise control and many more. The increasing trend in automotive electronics shows few signs of abating. It has been estimated that electronics will account for as much as 40% of a car’s bill-of-materials by the end of this decade. The modern car can now truly be described as a “networked computing platform,” and the theme will highlight this issue in the context of electronic design automation.

The program this year includes a new WACI (Wild and Crazy Ideas) session, presenting early expositions of non-incremental ideas. The papers in this session encourage out-of-the-box thinking and are designed to promote discussions among attendees during and after the session.


A major theme this year includes a strong focal point built around system-level design, including system-level communication issues aimed at designing the communication infrastructure of complex systems-on-chip, sessions that highlight industrial applications of ESL methods, MPSoC design, transaction level modeling, and 1000 core chips. Sessions in the area of embedded systems present the latest in embedded hardware and software design methods.

The business track is driven by an all-day track on Tuesday, beginning with a morning keynote, and continuing with an all-day management seminar presented by a group of luminaries: Geoffrey Moore, Raul Camposano and Jim Smith.

Design for manufacturability issues are prominent throughout the program, continuing a trend that has been active for several years now. In addition, there are sessions on process-aware physical design, statistical timing analysis, bridging the gap with silicon, and modeling the impact of technology on design. These are supplemented by special sessions on silicon measurement, and the design-manufacturing interface.

Low power is a prominent design consideration, and several sessions in the technical program focus on issues in this area. This year’s selection of papers on power analysis and low power design covers a broad range of topics of wide interest for practical applications and workflows, with sessions dedicated to issues related to leakage power and implications of design variability on full-chip leakage power, on circuit-level approaches for low power design, and on tools and methodologies of interest to system-level design.

Another strong component of the program is in the area of verification. This year’s program includes some outstanding papers on improving the verification process, ranging from theoretical results on the core computational engines of verification tools to practical, “best practice” case studies on the successful use of cutting-edge verification methodologies.

The technical paper presentations on Tuesday through Thursday are complemented by 7 tutorial presentations on Monday and Friday. These are presented by experts in the field, and cover themes such as DFM and variability, system level design, formal verification, reliability under soft errors, low power design, and power delivery concerns for die and package design. The 6 hands-on tutorials are in the area of DFM.

An array of panels spread throughout the program allow for free-form discussions headed by luminaries in the field, addressing emerging and important areas in the field of EDA. The panels cover topics such as EDA megatrends under shortening consumer cycles, handoffs between design and manufacturing, early power-aware design, transaction-level modeling, IP issues, multicore design, and challenges in functional verification. Pavilion panels on the exhibit floor lay the basis for more free-flowing and informal discussions. Topics of this year’s panels include trends in EDA, managing mixed-signal designs, DFM, system-level wireless design, anticipating the next killer app, and many more.
Sunday, June 3

- 4th UML for SoC Design Workshop - 9:00am - 5:30pm
- Low Power Coalition Workshop - Standards for Low Power Design Intent - 12:30pm - 3:30pm

EDA Consortium Executive Reception at the San Diego Marriott Hotel and Marina 5:00pm - 7:00pm

Monday, June 4

Free Monday Exhibit Hours 9:00am - 6:00pm

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Monday Keynote

Lawrence D. Burns - Vice President of Research & Development and Strategic Planning, General Motors Corp.

2:00 - 3:30pm • Ballroom 20ABC

Tuesday, June 5

Exhibits Hours 9:00am - 6:00pm

8:30 to 10:15

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<th>Rm: 6E</th>
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<td>10:30</td>
<td>SPECIAL SESSION: Trusted Hardware</td>
<td>PANEL SESSION: Mega Trends and EDA 2017</td>
<td>Industrial Application of System Level Methods</td>
<td>Novel Techniques for Interconnect</td>
<td>Formal &amp; Semi-Formal Verification Techniques</td>
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<td>12:00</td>
<td>LUNCH 12:00pm - 2:00pm</td>
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<td>2:00</td>
<td>Leaksage Power Analysis &amp; Optimization</td>
<td>PANEL SESSION: Making Manufacturing Work for You</td>
<td>Energy &amp; Performance Issues in On-Chip Communication Networks</td>
<td>Circuit Simulation</td>
<td>Signal &amp; Power Delivery Integrity</td>
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BREAK 4:00pm - 4:30pm

Wednesday, June 6

Exhibits Hours 9:00am - 6:00pm

8:30 to 10:00

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<td>10:30</td>
<td>INVITED SESSION: Silicon, Safety and Self-Driving Cars</td>
<td>SPECIAL SESSION: Silicon Measurement Correlation to Reliability</td>
<td>Optimizing Arithmetic &amp; Communication</td>
<td>Analog &amp; RF Simulation</td>
<td>PANEL SESSION: TLM: Crossing Over from Buzz to Adoption</td>
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<td>12:00</td>
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<td>PANEL SESSION: Electronic: The New Differential in the Automotive Industry</td>
<td>Modern Placement Techniques</td>
<td>Advances in Embedded Hardware Design</td>
<td>Bridging the Gap with Silicon</td>
<td>Practical Solutions for Power-Aware Testing</td>
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<td>4:30</td>
<td>SPECIAL SESSION: Virtual Automotive Platforms</td>
<td>SPECIAL SESSION: The Future of Interconnects</td>
<td>Advances in Decision Procedures</td>
<td>3D IC &amp; Package Design Issues</td>
<td>PANEL SESSION: Corezilla: Build and Tame the Multicore Beast</td>
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BREAK 4:00pm - 4:30pm

4:30 to 6:30

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Wednesday Night Party - 7:00pm • 10:00pm • Sails Terrace, San Diego Convention Center

Presenters will be available in Room 1A/B for additional 20-minute question-and-answer periods after each session.

Special interest to designers:
- Analog/Mixed-Signal/RF and Simulation Business
- Design and Verification of Low Power ICs
- Hardware Dependent Software (HDS)
- Low Power Design
- Automotive Themes
- New and Emerging Technologies
- Physical Design
- Synthesis and FPGA
- System Level and Embedded
- Verification and Test

IEEE's Distinguished Speaker Lecture and Reception in the Sails Pavilion from 6:00pm - 8:00pm

Tuesday Council on EDA's
The automotive industry stands on the threshold of a new opportunity – an opportunity that stems from the reinvention of the automobile using a new DNA that exchanges the internal combustion engine, petroleum, and mechanical linkages for fuel cells and batteries, hydrogen and electricity, and electronic systems and controls. Electrically driven vehicles and the introduction of advanced electronics and connected vehicle technologies will revolutionize how our vehicles operate, how we interact with them, and how they communicate with each other and the outside world. These new technologies will also, importantly, dramatically change how automobiles are designed and built.

In this talk, Dr. Burns will highlight why the new automotive DNA will be paradigm shifting for industry and address the design challenges and opportunities presented by the new real-time electrical, electronics-based architectures, systems, and software for our vehicles.
Innovation or Extinction - The Choice Is Yours!

Innovation is critical for the long term success of any industry. The electronics, semiconductor and the electronic design automation industry have pursued such a strategy successfully for several decades. This was made possible by technological advances in processing, innovative design flows and methodologies, and continued improvement in design tools. However limitations in manufacturing, increasing design complexity and design costs have started to decelerate the possible gains obtained along these directions. In the recent past, growth in these industries has slowed and it is perceived by many that these industries need to do more to capture growth. In addition, the twin forces of globalization and technology have led to changes that impact business dramatically. Globalization has enabled vast and eager talent pools to participate in business at low cost. Technology has lowered the barriers to market entry and leveled the playing field in many situations. In the current scenario, innovation is not only key but perhaps the only method to differentiate your business from that of your competitors. Registration for this event includes: entrance to the Keynotes, entrance to the Exhibition, a copy of the book “Dealing with Darwin”, coffee breaks, and the Productivity Impact Luncheon (produced by EDA Consortium and FSA).

Automotive Electronics at DAC

There has been an explosion of electronic content in automobiles recently. For 2007, the automotive semiconductor market is expected to be $19 billion (Gartner/Dataquest) and by 2010 it is expected that 40% of the bill of materials in cars will be for electronics. The Automotive theme addresses how design tools and methodologies will be used to support the tremendous design requirements for automotive electronics and what role EDA will play.

Attendees will have a chance to “look under the hood” of two cars that represent the future of automotive electronics: the GM Sequel Hydrogen car and the Wrightspeed X1 prototype electric car with a few lucky attendees getting a chance to test drive the Sequel Hydrogen car.

Technical presentations from experts representing leading electronics and automotive suppliers include a Keynote address by Larry Burns, Vice President Research & Development and Strategic Planning for General Motors. Other technical presentations include sessions on Automotives Electronics as a major product differentiator, the network protocols for distributed architectures, safety issues including robust design, and the validating and testing of automotive software. Additional sessions address the use of virtual platforms to reduce MCU count, embedded systems design as a competitive advantage, and how the changing requirements of the automotive market will affect the R&D agendas of semiconductor and EDA companies.

DAC Pavilion on the Exhibit Floor  Booth #6360

DAC has an exciting line-up of panels and presentations in the DAC Pavilion on the exhibit floor. The DAC Pavilion sessions are open to all attendees and feature provocatively technical, business, and strategy discussions.

44th DAC Workshops

4th UML for SoC Design Workshop
Sunday, June 3, 9:00am - 5:30pm

Low Power Coalition Workshop - Standards for Low Power Design Intent
Sunday, June 3, 12:30pm - 3:30pm

Design and Verification of Low Power ICs
Sunday, June 3, 4:00pm - 7:00pm

Hardware Dependent Software (HdS)
Sunday, June 3, 1:00pm - 7:30pm

Introduction to Chips and EDA for a Non-Technical Audience
Monday, June 4, 10:00am - 12:00pm

Workshop for Women in Design Automation - Managing Your Career
Monday, June 4, 9:00am - 1:45pm

3rd Integrated Design Systems Workshop
Monday, June 4, 12:00pm - 5:00pm
Exhibition

The 44th DAC Exhibition is located on the main floor of the San Diego Convention Center.

The exhibition floor is bursting with 240 vendors offering products for all phases of the electronic design process including EDA tools, IP cores, embedded system and system-level tools, as well as silicon vendors and design-for-manufacturing companies. The DAC show floor features its unique exhibit booth and private suite combination, which gives you the freedom to deeply explore the products on the show floor and find a solution that is right for your design flow. Visit the DAC exhibition and find out how you can improve performance and shorten the time-to-market on your next design.

Attend Free Monday, June 4, 2007

Exhibit Hours
Monday-Wednesday, June 4-6
9:00 am - 6:00 pm
Thursday, June 7
9:00 am - 1:00 pm

Exhibiting Companies (as of April 27, 2007)

Accelicon Technologies, Inc.
ACE Associated Compiler Experts bv
Agilent Technology Solutions
Aldic, Inc.
Algotronics Ltd.
Altos Design Automation
Analog Bits Inc.
Analog Rails
Anssat Technology, Inc.
Anchor Semiconductor, Inc.
Ansoft Corp.
Apache Design Solutions, Inc.
Applied Imagination Technology
Applied Wave Research, Inc.
Applicare International
Arsan Chip Systems
ArchPro Design Automation, Inc.
ARM
Artwork Conversion Software, Inc.
Athena Design Systems
AtopTech
Atosfera Inc.
Attechmate
aEstrimicrosystems
AutoESL Design Technologies Inc.
Averant, Inc.
Avery Design Systems, Inc.
Axion Design Automation
Azuro, Inc.
Beach Solutions Ltd.
Berkeley Design Automation, Inc.
Blaze DRM Inc.
Blue Pearl Software
Bluespec, Inc.
Breaks Veronica Systems
Brion Technologies
BuildDigital s.r.l.
CAD Science, Inc.
Cadence Design Systems, Inc.
Calypso Design Systems
Carbon Design Systems, Inc.
CAST
Center for Embedded Computer Systems
Centrics Inc.
Chip Estimate Corp.
ChipVision Design Systems
Cirrus Logic
Clear Shape Technologies, Inc.
CiscoSoft, Inc.
CLK Design Automation, Inc.
CMF
Coherent Design
CommandCAD, Inc.
Concept Engineering GmbH
Coulping Wave Solutions
CoWare, Inc.
CRC - Press - Taylor & Francis
CriticalBlue
DAC Pavilion
Dataram
DATE ‘08
DeFacto Technologies
Denali Software, Inc.
Design and Reuse
Dini Group (The)
Dolphin Integration
Douglas System Co., Ltd.
EDAC, Inc.
Elsevier
EMA Design Automation, Inc.
eMemory Technology Inc.
ENOVIA MatrixOne
Encasy Technology, Inc.
Envision Technology
EVE
Evolveable Systems Research Institute, Inc.
Extension Media
Extreme DA
Fenix Design Automation
FishTail Design Automation
Flomerics, Inc.
Forte Design Systems
Fortland Inc.
FSA
FTL Systems, Inc.
Gaster Research AB
GateRocket, Inc.
GeneSys Testware, Inc.
Gidel Inc.
Golden Gate Technology, Inc.
Gradient Design Automation
Handshake Solutions
HARDI Electronics AB
Helix S.A.
Heller Ehrman
Hewlett-Packard Co.
Hummimgbird Connectivity - Open Text
IBM Corp.
IC Manage
IEEE Spectrum
IMEC
Imperas, Inc.
Incendia Design Systems, Inc.
Innovative Silicon Inc.
InsideChip.com
Intel Corp.
Intellitech Corp.
InternetCAD, Inc.
Interia Systems, Inc.
Jasper Design Automation, Inc.
Javelin Design Automation, Inc.
JEDA Technologies
KETI / IP SOC Support Center
Kilopass Technology, Inc.
Kimolion Technologies Inc.
Knowlent Corp.
Laflin Instigate
Legend Design Technology, Inc.
Library Technologies, Inc.
Lightspeed Logic
LogicVision, Inc.
Lorenz Solutions
Lynquint, Inc.
Magillem Design Services
Magma Design Automation, Inc.
Manhattan Routing Inc.
MasiTech LLC
MathWorks, Inc. (The)
McGraw-Hill Professional
Mentor Graphics Corp.
Micro Magic, Inc.
Microlagic Design Automation
Miranbis Design Inc.
Mixel, Inc.
MOSAIQ
MOSIS
MunEDA GmbH
MyCAD, Inc.
Nangate
Nannor Technologies, Inc.
Nanovate Design Automation
Nascentric, Inc.
National Instruments Corp.
NEC Information Systems, Ltd.
NEC System Technologies, Ltd.
Novas Software, Inc.
Novelics
Nysym Technology Inc.
Obsidian Software, Inc.
OPC International Partnership
OEA International Inc.
OneSpin Solutions GmbH
Open IT, Inc.
Optimal Corp.
Orora Design Technologies, Inc.

Pextra
Platform Computing
PLD Applications (PLDA)
Plurality Ltd.
Ponte Solutions, Inc.
Prentice Hall Prof. / Pearson Education
ProDesign Electronic Corp.
Progenit Design, LLC
Prolific, Inc.
Pulsic Ltd.
Pvot Technology, Inc.
QThink
Quad-Pak
Real Intent, Inc.
Reed Business Information
Rio Design Automation
Runtime Design Automation
Sagegate
Santec
Sandwork Design Inc.
Saratoga Data Systems, Inc.
Sarnoff Europe
Semifone, Inc.
Sequence Design, Inc.
Shearwater Group, Inc. (The)
S2
Sierra Design Automation, Inc.
SIGDA/DAC University Booth
Sigtryg, Inc.
Silicon Canvas Inc.
Silicon Design Solutions
Silicon Frontline Technology
Silicon Hive
Silicon Image
Silicon Navigator
Silistix, Inc.
Simcad Design Automation Inc.
Softlin Technology Pvt. Ltd.
Solido Design Automation Inc.
Space Codegen
Springer
StarNet Communications
Swati Design Automation, Inc.
Synchronic
SynCira
Synfar, Inc.
Synopsys, Inc.
Synopsys, Inc. - Partner Booth
SYngility, Inc.
SynTest Technologies, Inc.
Takumi Technology Corp.
Tanner EDA
Target Compiler Technologies
Tata Elxsi
TeamEDA, Inc.
TechForce, Inc.
Tekleks
Time To Market Inc.
TOOL Corp.
Triad Semiconductor, Inc.
True Circuits, Inc.
TSML
TSS - Test Systems Strategies, Inc.
Tuscan Design Automation, Inc.
UMC
Unique1cs, LLC
Uniquely, Inc.
V&IT Systems Technology
VeriEZ Solutions, Inc.
Verific Design Automation
Vertool, Inc.
VIASIC Inc.
Virage Logic Corp.
Xoomsys
XALYS
Z Circuit Automation, Inc.
Zeland Software, Inc.

Detailed conference and exhibition information is now available on-line: www.dac.com. Register today!

QUESTIONS? call 800-321-4573
REGISTER TODAY!  Register on-line through June 8, 2007

Registration Options:
- Register on-line Internet registration open through June 8. Mail/Fax registrations not accepted after May 25.
- Free Monday Exhibit-Only to visit the Exhibition, on Monday only, June 4.
- Keynote Addresses on Monday, Tuesday, and Thursday are open to all.
- Exhibit-Only allows admittance to the Exhibition, Monday through Thursday.
- Full Conference includes all three days of the Technical Conference, access to the Exhibition Monday through Thursday, and the 44 Years of DAC DVD Proceedings, plus a ticket to the Wednesday Night Party.
- Student includes all three days of the Technical Conference, access to the Exhibition Monday through Thursday, and the 44 Years of DAC DVD Proceedings, plus a ticket to the Wednesday Night Party.
- One-/Two-Day Registration includes the day(s) you select for the Technical Conference, Monday through Thursday of the Exhibition, and the 44 Years of DAC DVD Proceedings.
- Full-Day Tutorials are offered on Monday, June 4, and Friday, June 8. You must register for at least one day of the Technical Conference to attend tutorials. Tutorial registration fee includes continental breakfast, lunch, coffee breaks, and tutorial notes.
- Hands-on Tutorials are three-hour tutorials utilizing hands-on software tools from DAC exhibitors. Attendees must register for a minimum of an Exhibit-Only registration to be eligible to attend Hands-on Tutorials. Due to the proprietary nature of the discussions, presenting companies reserve the right to refuse access to employees or contractors of competitors. Space is limited.

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<th>TUTORIALS</th>
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<tbody>
<tr>
<td>Full-Day Tutorials</td>
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<td>Standards for Low Power Design Intent</td>
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