RESEARCH MANUSCRIPT SUBMISSION
DAC is the premier event to showcase cutting-edge research achievements in the design and design automation of electronic circuits and systems. The 2018 edition of DAC welcomes high-quality manuscripts on design automation algorithms, tools and methodologies (EDA track), the design of circuits, architectures, and systems (DES track), embedded systems and software (ESS track). Areas of special interest include automotive electronics, security, machine learning, and the Internet-of-things. Acceptance rates for manuscript publication are uniform across all topic areas and have been around 21-23% for the past several years.

Abstract Deadline – November 14, 2017
Manuscript Deadline – November 21, 2017

DESIGNER/IP TRACK SUBMISSION
This year, Designer Track and IP Track are interweaved to encourage more synergy between designers and IP developers, will include presentations, poster sessions and a rich set of invited talks/panels to facilitate information exchange and interactions. It offers a unique opportunity to network with and learn from other industry experts about best practices and current trends.

Submission Deadline – January 23, 2018

PANEL PROPOSAL
A good panel session explores a single high-level issue or question and includes representatives of differing viewpoints. Controversy is appropriate and encouraged. The topic should be relevant to multiple segments of DAC attendees. This year DAC has the following topic areas: Automotive, Design, EDA, ESS, IoT, IP, Machine Learning/Al, and Security/Privacy.

Submission Deadline – November 21, 2017

SPECIAL SESSION PROPOSAL
DAC seeks proposals for the Research Track Special Sessions, Designer Track Invited Presentations and IP Track Invited Presentations. These sessions intend to highlight timely, visionary and emerging topics for the respective audience.

Submission Deadline – November 21, 2017

TUTORIAL PROPOSAL
DAC is looking for timely, relevant and tangible hands-on topics that provide immediate value and learning for its attendees on design, methodology, design automation fundamentals, as well as automotive electronics, hardware and embedded system security, machine learning, and Internet of Things.

Submission Deadline – November 21, 2017

WORKSHOP PROPOSAL
DAC invites you to submit a workshop on emerging topics related to design, design methodologies, and design automation as well as automotive electronics, hardware and embedded system security, machine learning and Internet of Things. Workshop proposals should address topics which span the interest of many suppliers and users, and are not specific to a single vendor for advertisement or endorsement of their technology.

Submission Deadline – November 21, 2017

COLOCATED CONFERENCE PROPOSAL
Save your attendees and your organization time and money by colocating your conference with the 55th Design Automation Conference in San Francisco, CA, June 24 - 28, 2018

Submission Deadline – November 21, 2017
The Internet of Things (IoT) continues to drive innovation in electronic systems. Its definition is inherently broad, encompassing everything from industrial automation, to wearable devices, to home security. Its components range from energy harvesters and smart sensors to data centers. What's needed to design these systems? IoT sessions at DAC aim to cover the entire spectrum, providing a holistic overview of IoT-related content in diverse areas, such as EDA methodologies and tools, reducing power consumption, security approaches and lightweight authentication, techniques for assembling needed IP for IoT systems, and methods for managing the complexity of automotive systems. Each of these has a role to play in the IoT design landscape and each is a challenging problem in and of itself.