

Event Schedule:

Workshop 9: DAC Workshop on Interdisciplinary Academia Industry Networking

Agenda

9:00-9:15: Welcome by the conference organizers (P. Haspel, Cadence, M. Huebner, Ruhr-University Bochum)

Academic view:

9:15-9:45: Prof. Ralf Sommer, TU-Ilmenau: Title: Seamless Design Methodology for heterogeneous Systems – Challenges for research, education, industry and EDA

9:45-10:05: Coffee Break:

10:05-10:35: Diana Goehringer, RUB, Title: System Level Development: An all virtual approach

10:35– 11:05: Matthew Swabey, Purdue University, Title: A holistic platform for education in ASIC design&manufacturing

Industrial view to academic networks:

11:05-11:50: Patrick Haspel, Cadence, USA: Title: To win in all situations: The Academic Network for EDA

11:50-12:30: Lunch break

Industrial view to academic networks continued:

12:30-13:15: Sadanad Gulwadi ARM, ARM, UK, Title: Intro to a VLSI Course – A SoC on the ARM Cortex-M0 IP for Academia

13:15-14:15: Coffee Break

14:15-15:00: Jason Wong, Xilinx Inc, San Jose: Title: Reconfigurable hardware in industrial and academic use cases

15:15-16:30: Panel: The needs and expectations of a fruitful industrial and academic cooperation:

Panelists: P. Haspel, Sadanad Gulwadi, Jason Wong, R. Sommer, D. Göhringer, Moderator: M. Hübner