

DAC 2015 SoC for HPC workshop

preliminary agenda

- 8:30 - 9:00 AM *Intro, greetings*
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- 9:00 - 9:30 AM *Welcome and overview*
 - John and Jim & DARPA, DOE, DOD, NSF statements
- *Keynote address 9:30 – 10:00*
 - ARM / AMD Semicustom (Scott Houghton)
- *System Integrators - Solution providers and/or users*
 - Describe the challenges, overview of what we would like to build
 - 10:00 - 10:20 Intel (Shekar Borkar)
 - 10:20 - 10:40 Sandia (Mike Holmes)
 - 10:40 - 10:50 *Break*
 - 10:50 - 11:10 ARM (Rob Aitken)
 - 11:10 - 11:30 ACS (Noel Wheeler)
 - 11:30 - 12:30 Discussion Topic 1: Requirements/Stakeholders
- *Lunch 12:30 - 13:00*
 - *Working lunch*
- *EDA Tool Vendors*
 - 13:00 - 13:20 Cadence (Chris Rowen)
 - 13:20 - 13:40 **Synopsys** (Waiting on response)
 - 13:40 - 14:00 **Open slot for additional EDA tool vendor (optional)**
- *IP / Misc (Integrating Open Technologies)*
 - 14:00 - 14:20 LBNL / OpenSoC (David D / Farzad)
 - 14:20 - 14:40 RISC-V (Krste)
 - 15:00 - 15:20 *Break*
 - 15:20 - 15:40 Broadcom (Giri Chukkapalli)
 - 15:40 - 16:00 **Open slot (optional)**
- *Discussion*
 - 16:00 - 17:00 Discussion Topic 2: Gap Analysis of Tools for creation of high-performance SoCs
 - 17:00 - 17:30 Wrap up (John and Jim)